

EAST - (10544454.wsp.1)

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(625) (program55 with delay53 with clock) and (output adj2 (register latch))

(65) (program55 with delay53 with clock) same (output adj2 (register latch))

(28) (program55 with delay53 with clock) with (output adj2 (register latch))

(366635) CAM

(0) CAM and ((program55 with delay53 with clock) with (output adj2 (register

(1) CAM and ((program55 with delay53 with clock) same (output adj2 (register

(16) CAM and ((program55 with delay53 with clock) and (output adj2 (register

(27458) (dll (delay53 near3 lock53 near3 loop) pll (phase near3 lock53 near3

(89056) output adj3 (register latch)

(461) clock adj2 pad

(21207) reference adj2 clock

(13) (clock adj2 pad) with (reference adj2 clock)

(5351) ((dll (delay53 near3 lock53 near3 loop) pll (phase near3 lock53 near3

(6) ((clock adj2 pad) with (reference adj2 clock)) and ((dll (delay53 near3

(1) (((clock adj2 pad) with (reference adj2 clock)) and (((dll (delay53 near3

(19) (US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-621

(11) (US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-62

(3) (US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-621

(3) ((US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-62

(0) (US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-621

(0) (US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-621

(10) ((US-6480557-S or US-6463109-S or US-6271681-S or US-6253346-S or US-62

(230) (program55 with delay53 with clock) and (output adj2 (register latch))

(5) (program55 with delay53 with clock) and (output adj2 (register latch)) a

(26) (program55 with delay53 with clock) and (output adj2 (register latch))

USPAT:US-PUB: LPO:PO: DERWENT, IBA, 106

Default operator DR

Highlights of 14 items only

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	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	3	4	5
1	US 6480557 B1	20021112	16	Portable GPS-receiver for a personal safety system	375/349			Rog, Andrey Leonidovich et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 6463109 B1	20021008	30	Multiple channel adaptive data recovery system	375/355	370/503; 375/371;		McCormack, Gary D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 6271681 B1	20010807	13	PCI-compatible programmable logic devices	326/41	326/101		Cliff, Richard G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 6253346 B1	20010626	8	Data transmission circuit having cyclic redundancy	714/758	375/225		Kim, Si Joong et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 6218864 B1	20010417	9	Structure and method for generating a clock enable	326/93	326/31; 326/38		Young, Steven P. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 5857095 A	19990105	18	Method for aligning a control signal and a clock	713/401	711/100		Jeddeloh, Joseph M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	US 5528186 A	19960618	10	Timing generator using digital signals to obtain	327/266	327/172; 327/263;		Imamura, Makoto	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	US 5260888 A	19931109	12	Shift and add digital signal processor	708/308			Sharman, Richard A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	US 6369624 B1	20020409	27	Programmable phase shift circuitry	327/156	327/147; 327/150;		Wang, Bonnie I. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	US 6286118 B1	20010904	29	Scan path circuitry including a programmable	714/726	714/724; 714/725		Churchill, Jonathan F. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	US 5146592 A	19920908	79	High speed image processing computer with overlapping	345/807	345/505; 345/545		Pfeiffer, David M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	US 20010050585 A1	20011213	14	Digital delay line with synchronous control	327/277			Carr, Larrie	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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